

Application Specific Discretes **DUAL TRANSIENT VOLTAGE SUPPRESSOR**
A.S.D.TM **FOR LINE CARD PROTECTION**

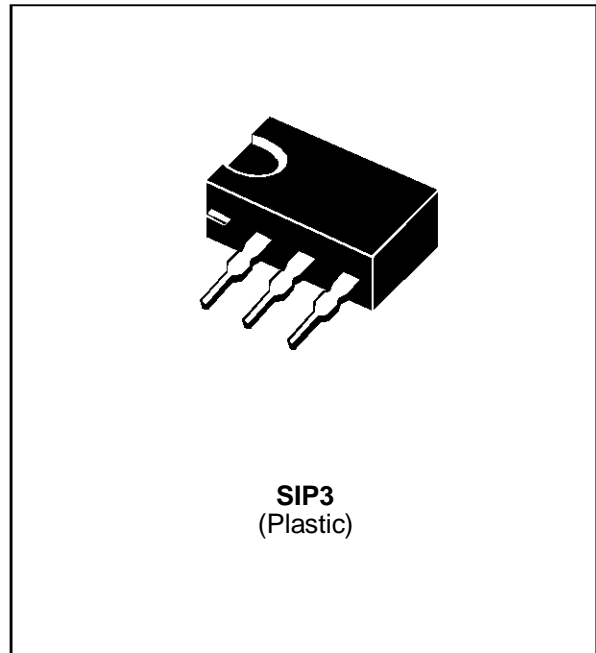
FEATURES

- DUAL BIDIRECTIONAL CROWBAR PROTECTION BETWEEN TIP and GND, RING and GND.
- PEAK PULSE CURRENT :
 $I_{PP} = 75A$ for 10/1000 μs surge
- HOLDING CURRENT :
 $I_H = 150mA$
- EXCELLENT DYNAMIC BEHAVIOUR WITH PARAMETERS SPECIFIED FOR DIFFERENT KINDS OF SURGES.

DESCRIPTION

The THBT200SD is a dual symmetrical transient voltage suppressor device. Offered in a SIP3 package, it ensures an excellent protection of telecommunication equipments against transient overvoltages caused by lightning strikes.

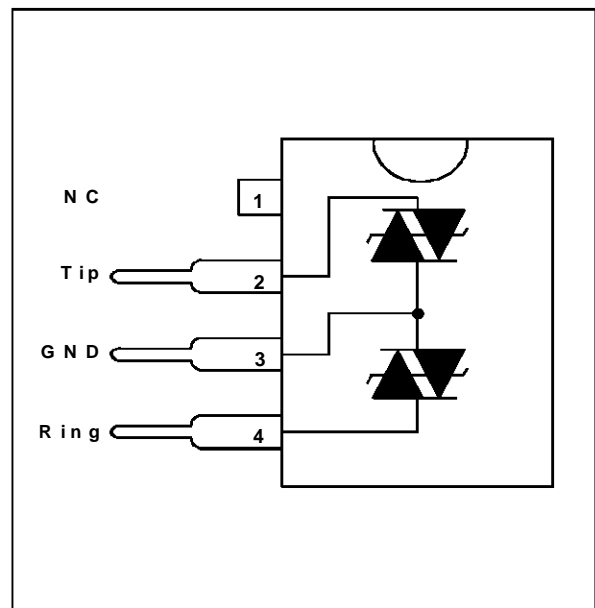
Dynamic characteristics have been defined for several types of surges, in order to meet the SLIC maximum ratings.



COMPLIES WITH FOLLOWING STANDARDS :

CCITT K20 :	10/700 μs	1.5kV
	5/310 μs	
VDE 0433 :	10/700 μs	2kV
	5/310 μs	
VDE 0878 :	1.2/50 μs	1.5kV
	1/20 μs	
FCC part 68 :	2/10 μs	2.5kV
BELLCORE		
TR-NWT-001089 :	2/10 μs	

FUNCTIONAL DIAGRAM



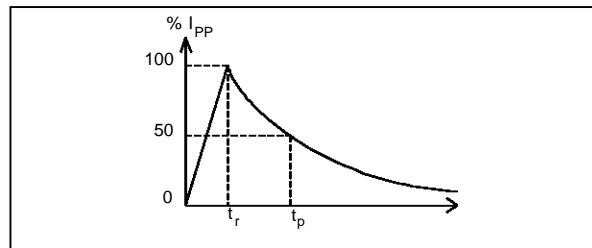
THBT200SD

ABSOLUTE MAXIMUM RATINGS (-40°C ≤ T_{amb} ≤ 85°C)

Symbol	Parameter		Value	Unit
I _{PP}	Peak pulse current	10/1000 μs 5/310 μs 8/20 μs 2/10 μs	75 125 150 225	A
I _{TSM}	Non repetitive surge peak on-state current (F = 50Hz)	t _p = 10ms t _p = 20ms t _p = 1s	35 30 12	A
di/dt	Critical rate of rise of on-state current	Non repetitive	100	A/μs
T _{stg} T _j	Storage temperature range Maximum junction temperature		- 40 to + 150 150	°C

Note 1 : Pulse waveform :

10/1000 μs	t _r =10 μs	t _p =1000 μs
5/310 μs	t _r =5 μs	t _p =310 μs
8/20 μs	t _r =8 μs	t _p =20 μs
2/10 μs	t _r =2 μs	t _p =10 μs



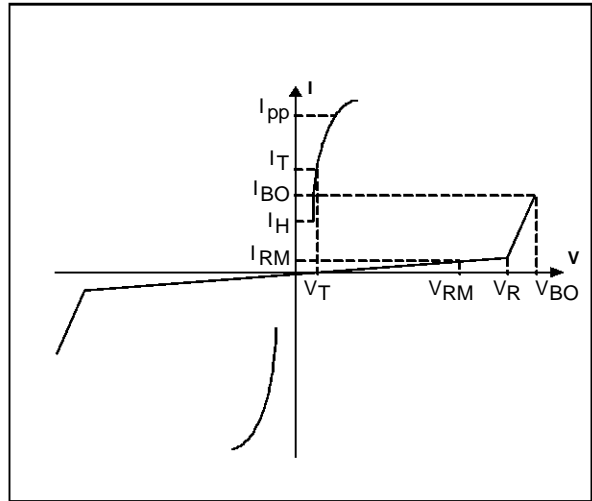
THERMAL RESISTANCE

Symbol	Parameter	Value	Unit
R _{th (j-a)}	Junction to ambient	70	°C/W

ELECTRICAL CHARACTERISTICS

($T_{amb} = 25^{\circ}C$, unless otherwise specified)

Symbol	Parameter
V_{RM}	Stand-off voltage
V_R	Reverse voltage
V_{BO}	Breakover voltage
I_H	Holding current
V_T	On-state voltage
I_{BO}	Breakover current
I_{RM}	Leakage current at V_{RM}
I_{PP}	Peak pulse current



STATIC PARAMETERS

Type	$I_{RM} @ V_{RM}$		$I_R @ V_R$		$V_{BO} @ I_{BO}$			I_H	V_T	C
	max		max		max	min	max	min	max	max
	μA	V	μA	V	V	mA	mA	mA	V	pF
THBT200SD	5	180	50	200	290	150	800	150	5	200

Note 1 : Measured at 50Hz.

Note 2 : See the functional holding current (I_H) test circuit.

Note 3 : Square pulse $t_p = 500 \mu s$, $I_T = 5A$.

Note 4 : $V_R = 1V$, $F = 1MHz$, 30mV bias.

DYNAMIC PARAMETERS

Symbol	Test conditions (see note 1)				Maximum	Unit
V_{BO}	10/700 μs	1.5kV	$R_p=10\Omega$	$I_{PP}=30A$	300	V
	1.2/50 μs	1.5kV	$R_p=10\Omega$	$I_{PP}=30A$	330	
	2/10 μs	2.5kV	$R_p=62\Omega$	$I_{PP}=38A$	380	

Note 1 : R_p is the protection resistor located on the line card.

THBT200SD

FUNCTIONAL HOLDING CURRENT (I_H) TEST CIRCUIT : GO-NO GO TEST

This is a GO-NO GO test which allows to confirm the holding current (I_H) level in a functional test circuit.

TEST PROCEDURE :

- Adjust the current level at the I_H value by short circuiting the D.U.T.
- Fire the D.U.T. with a surge current : $I_{PP} = 10A, 10/100\mu s$.
- The D.U.T. will come back to the OFF-state within a duration of 50ms max.

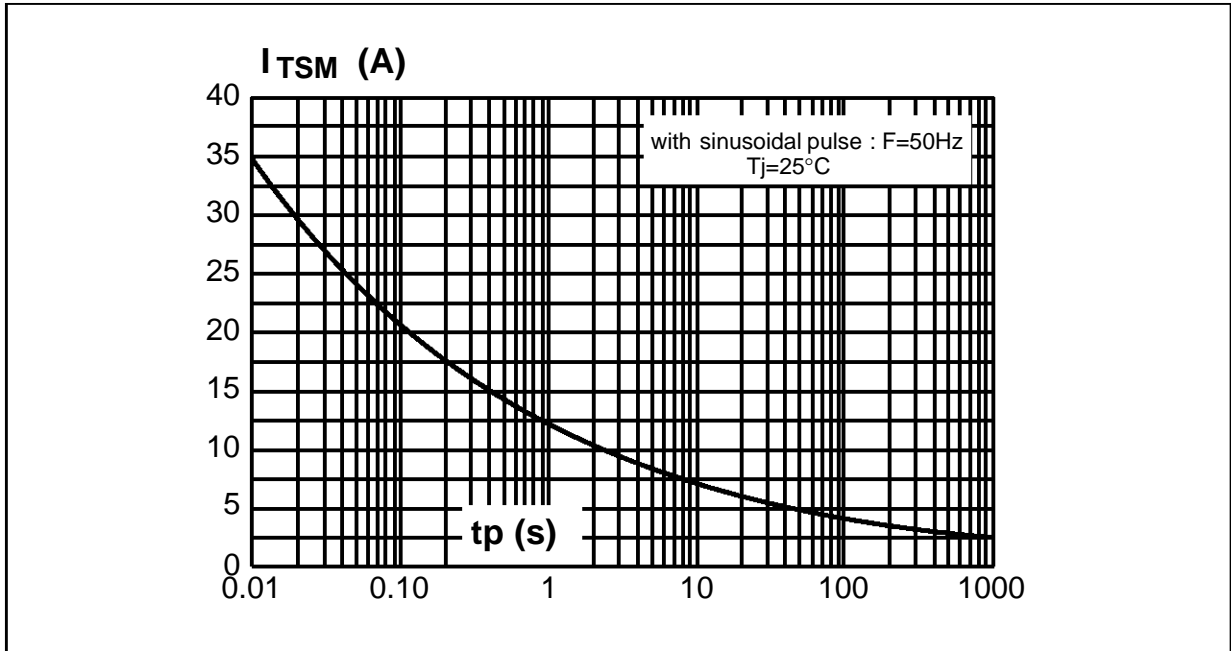
TEST CIRCUIT FOR V_{BO} DYNAMIC PARAMETERS

(V_P is defined in unload condition)

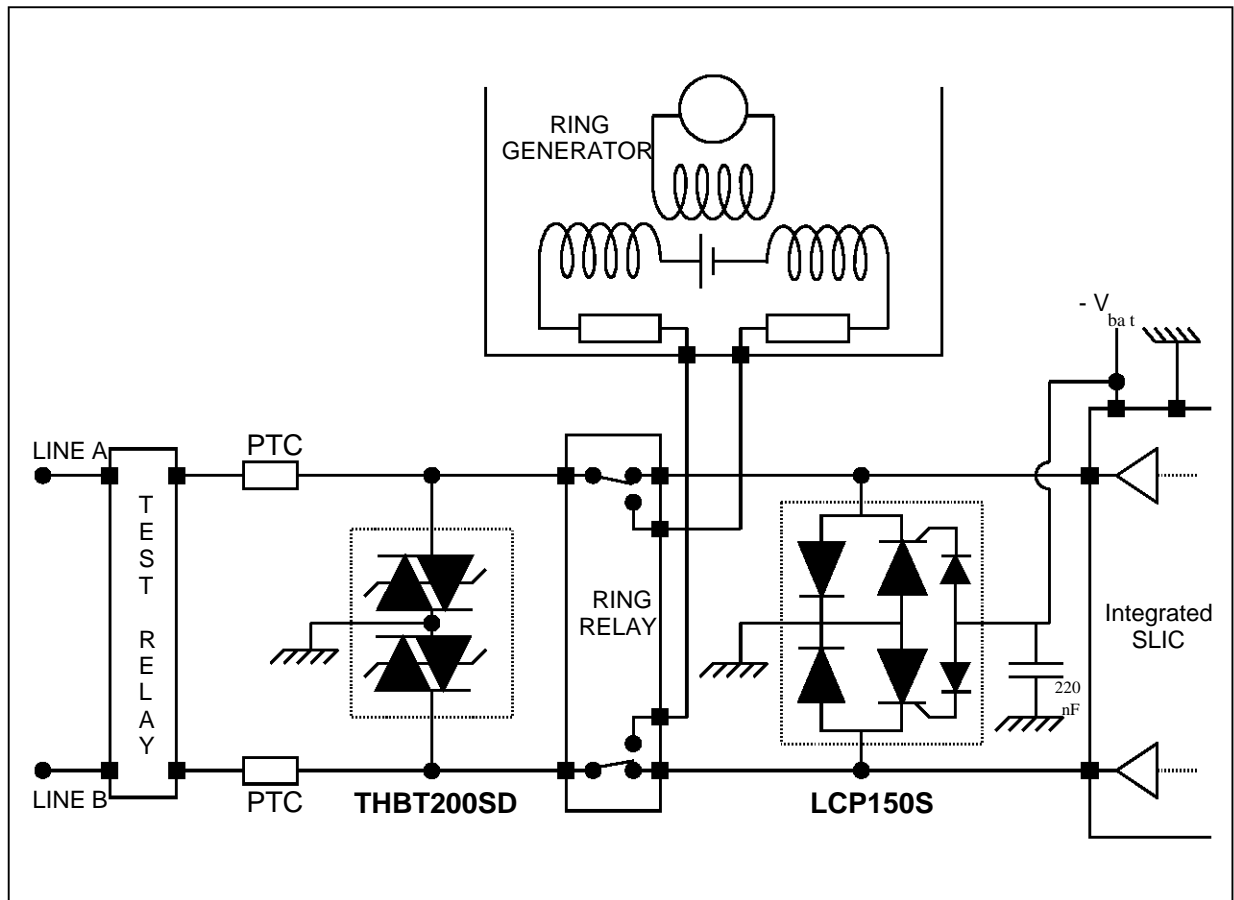
Pulse (μs)		V_p (V)	C_1 (μF)	C_2 (nF)	L (μH)	R_1 (Ω)	R_2 (Ω)	R_3 (Ω)	I_{PP} (A)	R_p (Ω)
t_r	t_p									
10	700	1500	20	200	0	50	15	25	30	10
1.2	50	1500	1	33	0	76	13	25	30	10
2	10	2500	10	0	1.1	1.3	0	3	38	62

R_p is the protection resistor located on the line card.

MAXIMUM NON REPETITIVE SURGE PEAK ON-STATE CURRENT



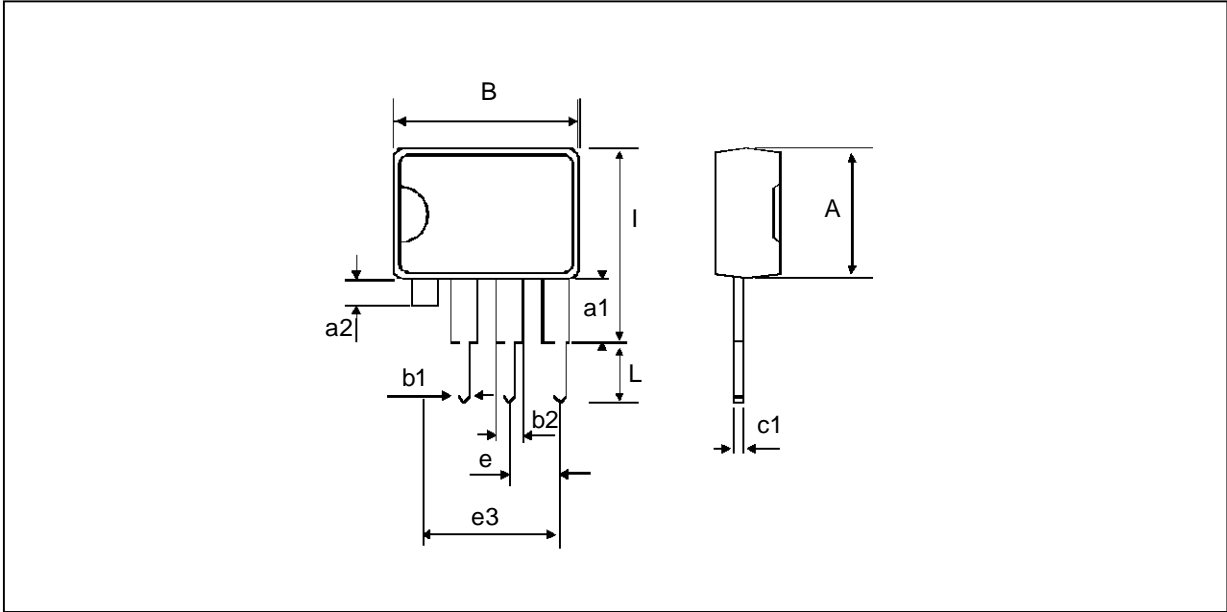
APPLICATION CIRCUIT : typical SLIC protection concept



THBT200SD

PACKAGE MECHANICAL DATA

SIP3 Plastic



REF.	DIMENSIONS					
	Millimetres			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			7.10			0.280
a1	2.80			0.110		
a2	1.50		1.90	0.059		0.075
B			10.15			0.400
b1		0.50			0.020	
d2		1.35	1.75		0.053	0.069
c1	0.38		0.50	0.015		0.020
e		2.54			0.100	
e3		7.62			0.300	
I			10.50			0.413
L		3.30			0.130	

MARKING :

Package	Types	Marking
SIP3	THBT200SD	TBT200SD

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